

DECLARATION AND POWER OF ATTORNEY

We, Bruce E. Hayden and William A. Shelly hereby declare that:

Our residences, post office address and citizenships are as stated below next to our respective names;

INVENTOR: Bruce E. Hayden
Address: 6415 W. Adobe Drive
Residence: Glendale, AZ 85308
Citizenship: United States of America

INVENTOR: William A. Shelly
Address: 4900 East Osborn Road
Residence: Phoenix, AZ 85018
Citizenship: United States of America

We believe we are the original and first inventors of the subject matter which is claimed and for which a patent is sought on the invention entitled **"COMPUTER PROCESSOR READ/ALTER/REWRITE OPTIMIZATION CACHE INVALIDATION SIGNALS"** described and claimed in the attached specification;

We have reviewed and understand the contents of the above-identified specification, including the claims; and

We acknowledge the duty to disclose information which is material to the examination of this application as defined in 37 C.F.R. 1.56.

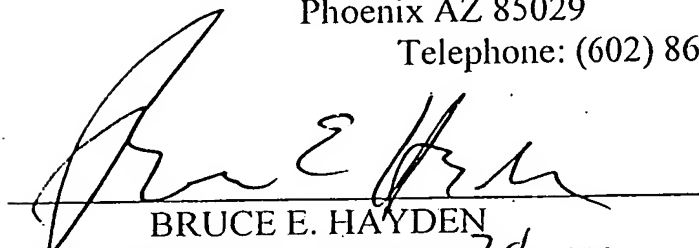
We hereby declare that all statements made hereby of our own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the

United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

We hereby appoint John S. Solakian (Reg. No. 25,087) and Bruce E. Hayden (Reg. No. 35,539) our attorneys with full power of substitution and revocation, to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith.

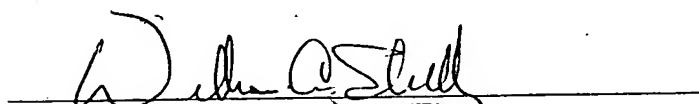
We request that all correspondence be directed to:

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Date of Execution: December 20, 2000

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WILLIAM A. SHELLY
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March 13, 2001

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